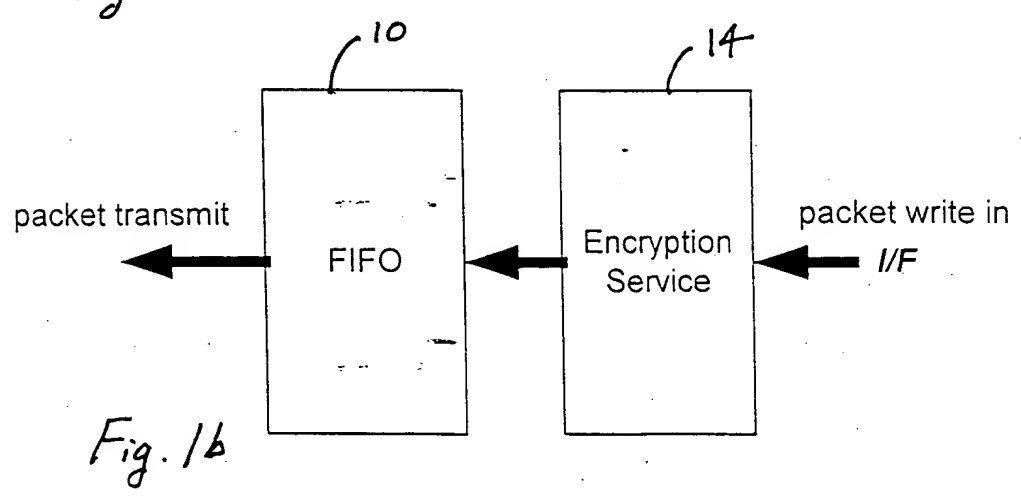
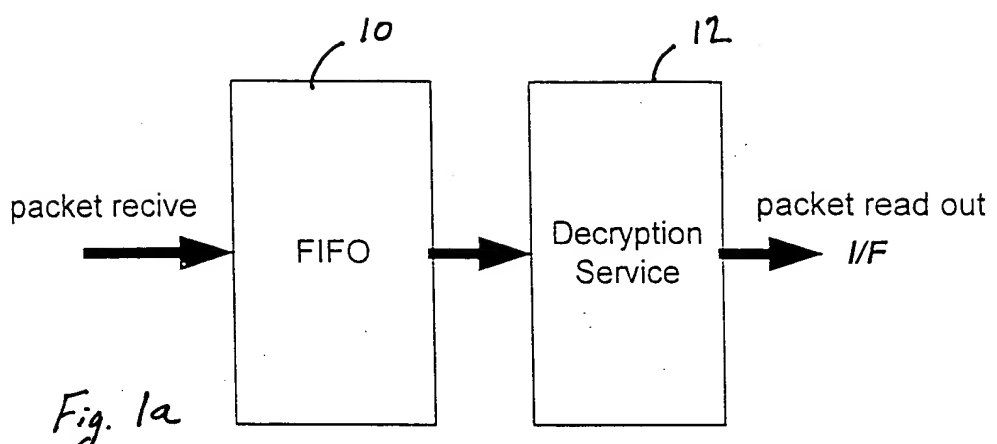


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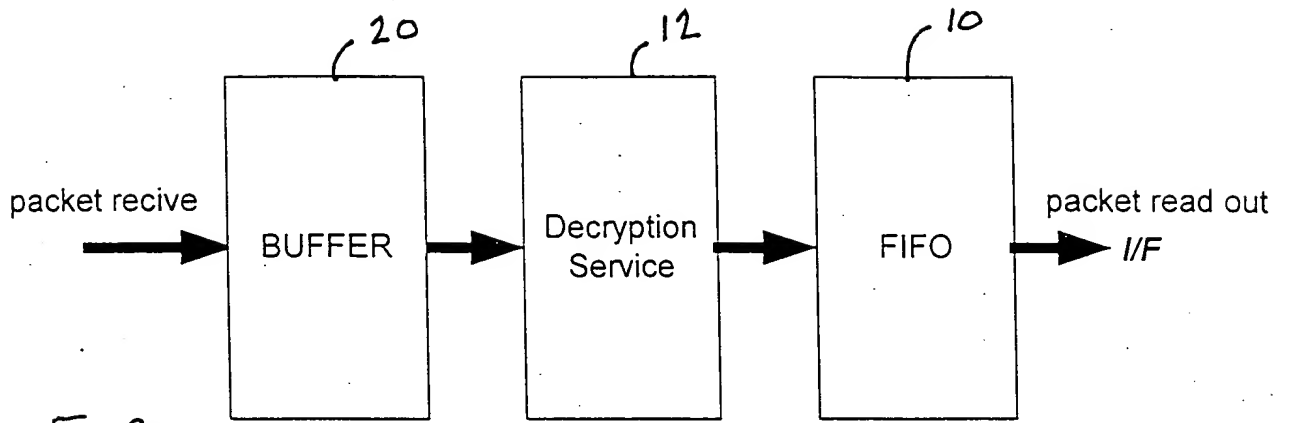


Fig. 2a

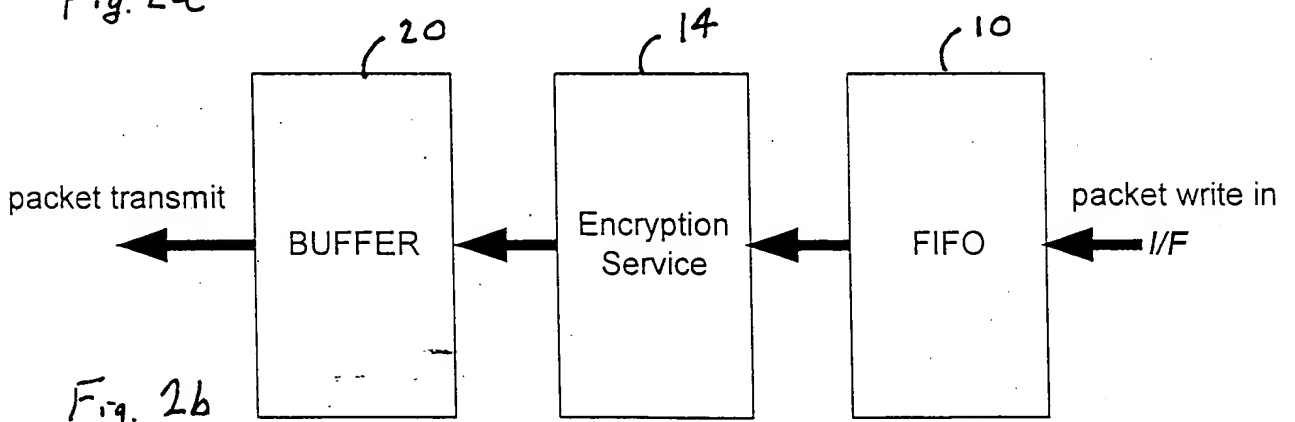


Fig. 2b

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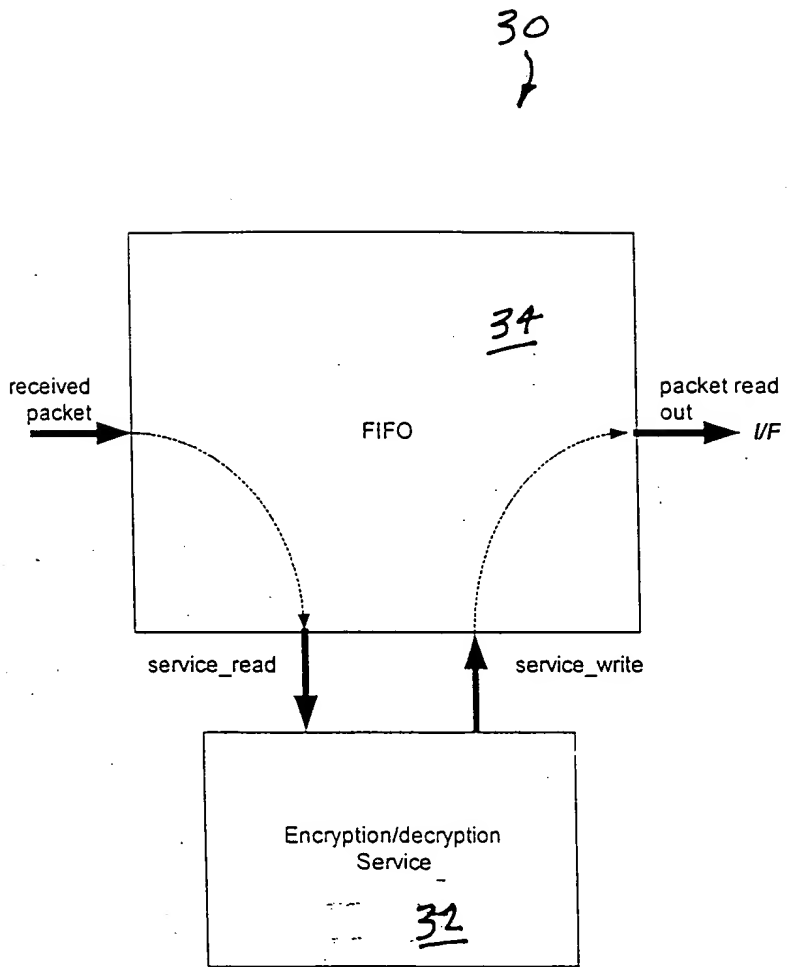


Fig. 3

The diagram illustrates a memory stack layout with the following components and labels:

- Unused area**: Located at the top and bottom of the stack.
- Primary write address (36)**: Points to the top boundary of the **Unprocessed Data** block.
- Unprocessed Data**: A block of data at the top of the active memory area.
- secondary read address (40)**: Points to the bottom boundary of the **Unprocessed Data** block.
- Cipher Block Size (42)**: A vertical double-headed arrow indicating the size of the **Data in Encryption/Decryption Service** block.
- Data in Encryption/Decryption Service**: A large central block of data.
- secondary write address (44)**: Points to the bottom boundary of the **Data in Encryption/Decryption Service** block.
- Primary read address (38)**: Points to the bottom boundary of the **Ready Available Data** block.
- Ready Available Data**: A block of data at the bottom of the active memory area.

Fig. 4

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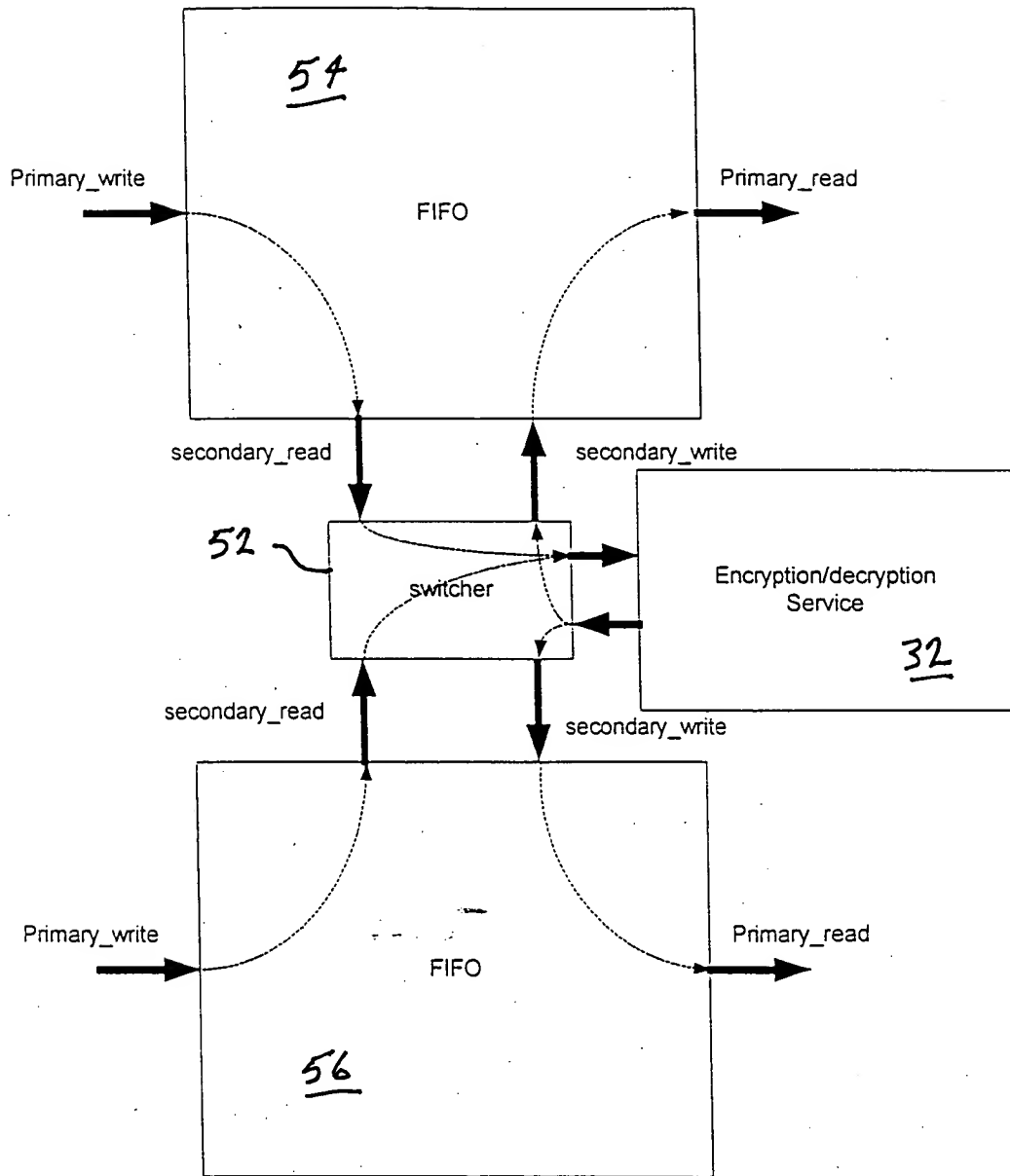


Fig. 5